

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	22	((("5062067") or ("5437037") or ("5502661") or ("5544067") or ("5696942") or ("5768567") or ("5784593") or ("5809283") or ("5862361") or ("5880975") or ("5978571") or ("5991523") or ("6052524") or ("6134516") or ("6135647") or ("6152612") or ("6175946") or ("6182258") or ("6223144") or ("6295517") or ("6321363") or ("6466898"))).PN.	US-PGPUB; USPAT	OR	OFF	2007/03/20 18:47
L2	427	703/13.ccor.	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2007/03/20 18:47
L3	686	703/14.ccor.	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2007/03/20 18:47
L4	495	703/22.ccor.	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2007/03/20 18:47
L5	109855	functional adj block	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2007/03/20 18:47
L6	26979	system adj level	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2007/03/20 18:47
L7	479	L6 with model	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2007/03/20 18:47
L8	56	L5 and L7	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2007/03/20 18:47
L9	36	L8 and object	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2007/03/20 18:47
L10	29	L9 and @ad<="20040408"	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2007/03/20 18:47

		Results
13.	((((((((((pub-date > 1959 and pub-date < 2005 and FULL-TEXT(system level) and FULL-TEXT(simulat*)) and hardware) and model) and function* block) and object) and optimiz*) and description language) and api) and interface) and compil*) and link*) and map*) and pin [All Sources(- All Sciences -)]	2
12.	((((((((((pub-date > 1959 and pub-date < 2005 and FULL-TEXT(system level) and FULL-TEXT(simulat*)) and hardware) and model) and function* block) and object) and optimiz*) and description language) and api) and interface) and compil*) and link*) and map* [All Sources(- All Sciences -)]	34
11.	((((((((((pub-date > 1959 and pub-date < 2005 and FULL-TEXT(system level) and FULL-TEXT(simulat*)) and hardware) and model) and function* block) and object) and optimiz*) and description language) and api) and interface) and compil*) and link* [All Sources(- All Sciences -)]	37
10.	((((((((((pub-date > 1959 and pub-date < 2005 and FULL-TEXT(system level) and FULL-TEXT(simulat*)) and hardware) and model) and function* block) and object) and optimiz*) and description language) and api) and interface) and compil* [All Sources(- All Sciences -)]	46
9.	((((((((((pub-date > 1959 and pub-date < 2005 and FULL-TEXT(system level) and FULL-TEXT(simulat*)) and hardware) and model) and function* block) and object) and optimiz*) and description language) and api) and interface [All Sources(- All Sciences -)]	76
8.	((((((((((pub-date > 1959 and pub-date < 2005 and FULL-TEXT(system level) and FULL-TEXT(simulat*)) and hardware) and model) and function* block) and object) and optimiz*) and description language) and api [All Sources(- All Sciences -)]	78
7.	((((((((((pub-date > 1959 and pub-date < 2005 and FULL-TEXT(system level) and FULL-TEXT(simulat*)) and hardware) and model) and function* block) and object) and optimiz*) and description language [All Sources(- All Sciences -)]	954
6.	((((((((((pub-date > 1959 and pub-date < 2005 and FULL-TEXT(system level) and FULL-TEXT(simulat*)) and hardware) and model) and function* block) and object) and optimiz* [All Sources(- All Sciences -)]	1,790
5.	((((((((((pub-date > 1959 and pub-date < 2005 and FULL-TEXT(system level) and FULL-TEXT(simulat*)) and hardware) and model) and function* block) and object [All Sources(- All Sciences -)]	3,211
4.	((((((((((pub-date > 1959 and pub-date < 2005 and FULL-TEXT(system level) and FULL-TEXT(simulat*)) and hardware) and model) and function* block [All Sources(- All Sciences -)]	7,397
3.	((((((((((pub-date > 1959 and pub-date < 2005 and FULL-TEXT(system level) and FULL-TEXT(simulat*)) and hardware) and model [All Sources(- All Sciences -)]	20,100
2.	((((((((((pub-date > 1959 and pub-date < 2005 and FULL-TEXT(system level) and FULL-TEXT(simulat*)) and hardware [All Sources(- All Sciences -)]	24,603
1.	((((((((((pub-date > 1959 and pub-date < 2005 and FULL-TEXT(system level) and FULL-TEXT(simulat*)) [All Sources(- All Sciences -)]	287,181

Find: [Documents](#)[Citations](#)Searching for **system level and simulation and hardware and functional block**.Restrict to: [Header](#) [Title](#) Order by: [Expected citations](#) [Hubs](#) [Usage](#) [Date](#) Try: [Google \(CiteSeer\)](#) [Google \(Web\)](#) [Yahoo!](#) [MSN](#) [CSB](#) [DBLP](#)5 documents found. **Order: number of citations.**VSDF: Synchronous Data Flow for VLSI - Kerihuel, McConnell, Rajopadhye (1994) (Correct) (3 citations)

We emphasize that our model is targeted at **system-level** RTL design, and not at individual component signal processing, synchronous data flow, VLSI **simulation** (R'esum'e :tsvp) This work was partially These systems suffer from the disadvantage for **hardware** implementation that blocks of data are buffered
ftp.inria.fr/INRIA/publication/publi-ps-gz/RR/RR-2337.ps.gz

One or more of the query terms is very common - only partial results have been returned. Try [Google \(CiteSeer\)](#).Tools for Correct DSP Synchronization - Kerihuel, al. (1993) (Correct)

operations of **functional blocks** at the **system level**, we use Signal. Signal is a synchronous Data Flow has been realised in several **simulation** environments from Berkely, the most recent circuits, are essential to modern digital **hardware** design. Thus we demand that our methodology be
ftp.inria.fr/INRIA/publication/publi-ps-gz/RR/RR-1973.ps.gz

A Concept for an Evaluation Framework for Reconfigurable Systems - Sawitzki, Spallek (1999) (Correct)

discussed above, hence it should be seen as a **system-level** approach. The design principles of the in the time-consuming development of dedicated **simulation** and prototyping environments, especially if the the following criteria for a generalized model. **Hardware** structure HS .This parameter describes
www.inf.tu-dresden.de/~ss9/fpl99.ps.gz

Fault Injection for Logic Synthesis Design using VHDL - DeLong, Ghosh, Johnson, al. (Correct)

dependability evaluation must be performed from **system level** design to gate level implementation in order monitor card in a distributed computer system. **Simulation** results illustrate the fault injection employed near the end of the design process after **hardware** and software prototypes have been developed. In
ftp.rstcorp.com/pub/papers/mugs95.ps

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